

FIG. 1

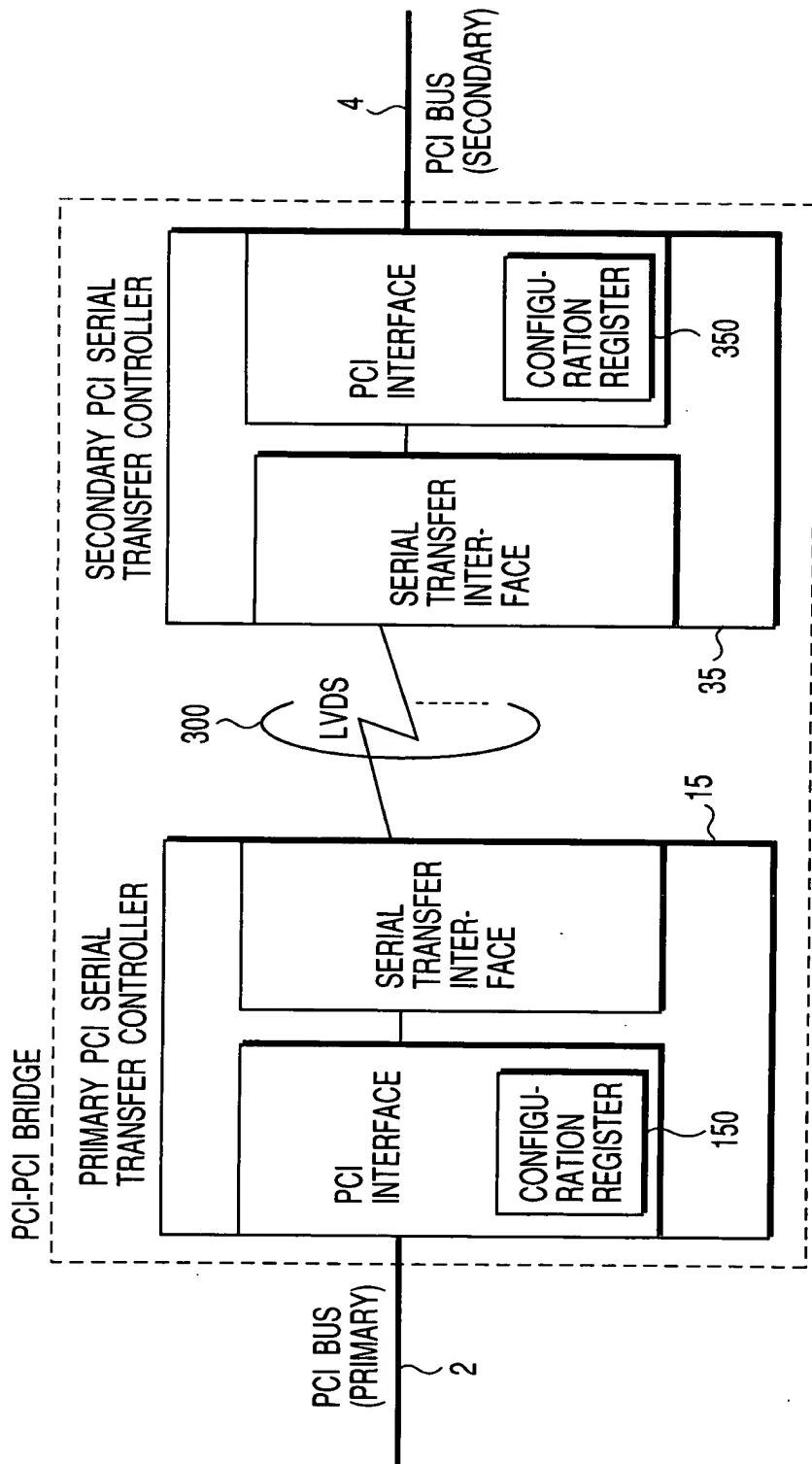


FIG. 2



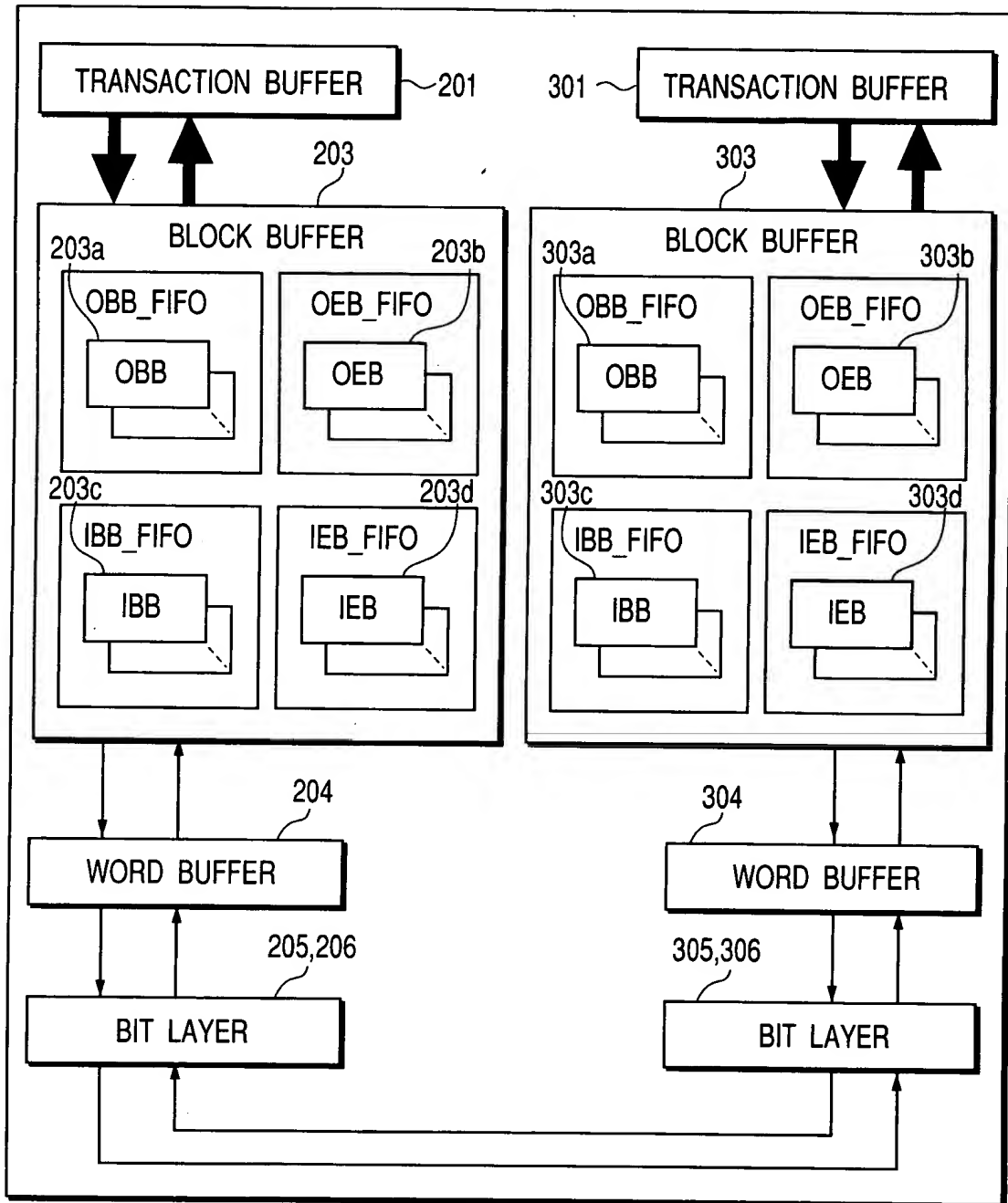


FIG. 4

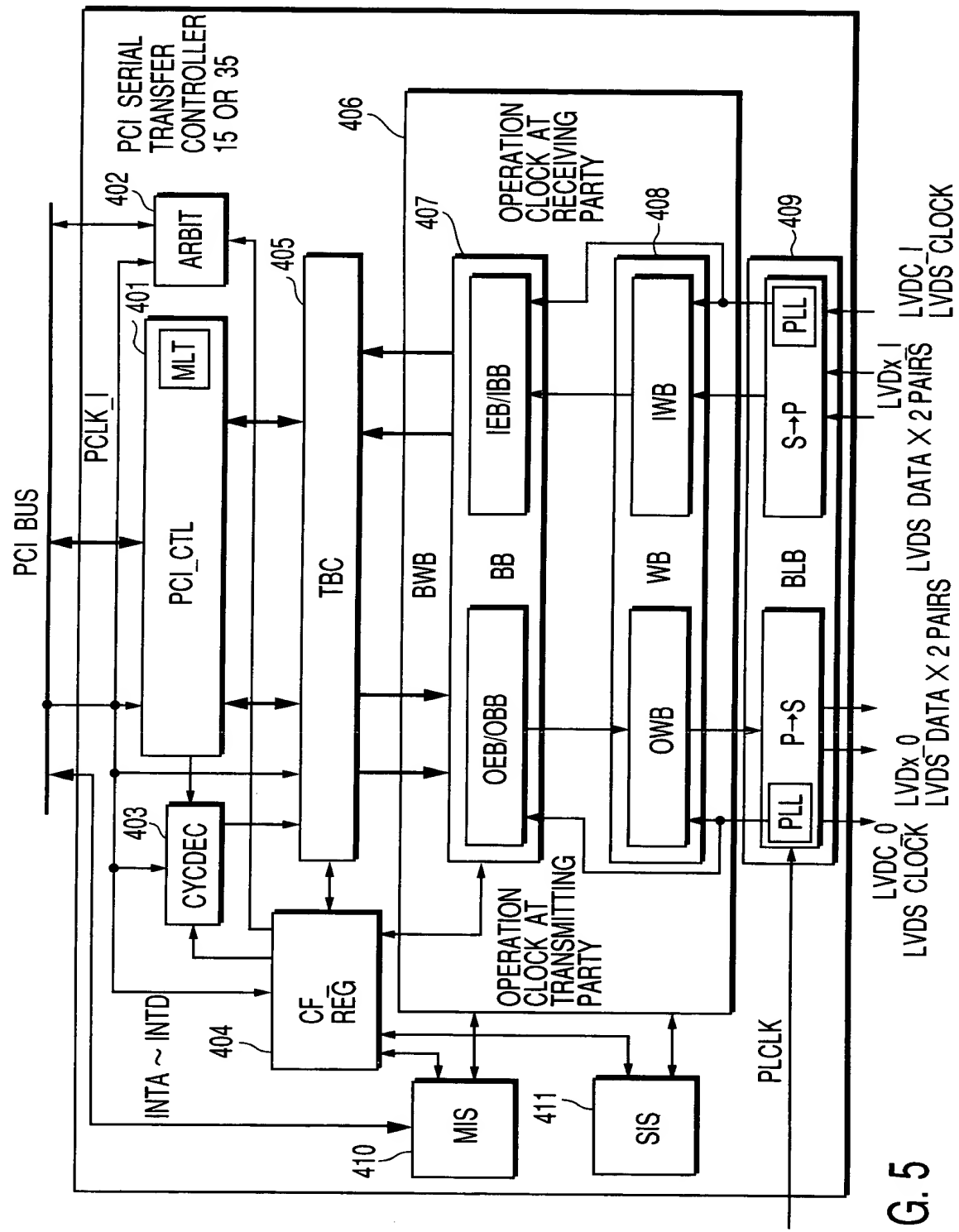
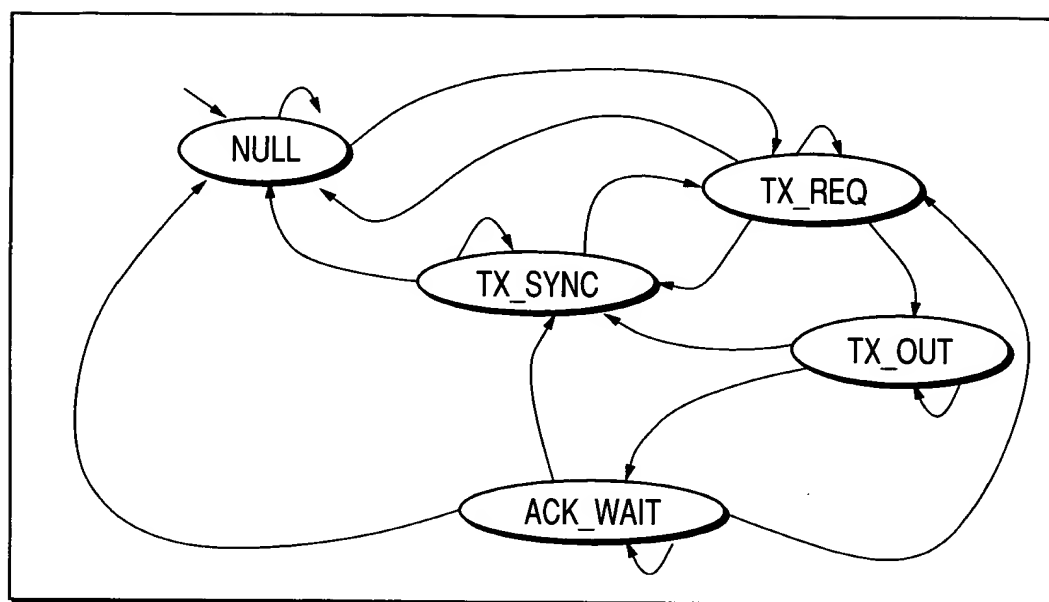


FIG. 5



## OBB STATE TRANSITION

FIG. 6

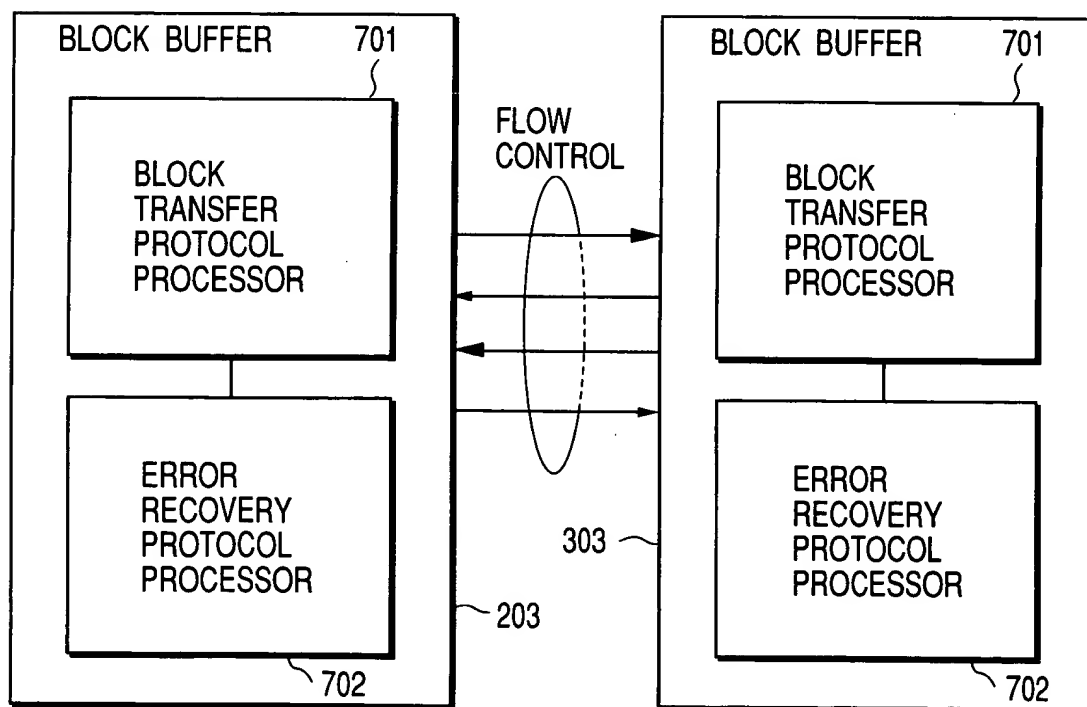


FIG. 7

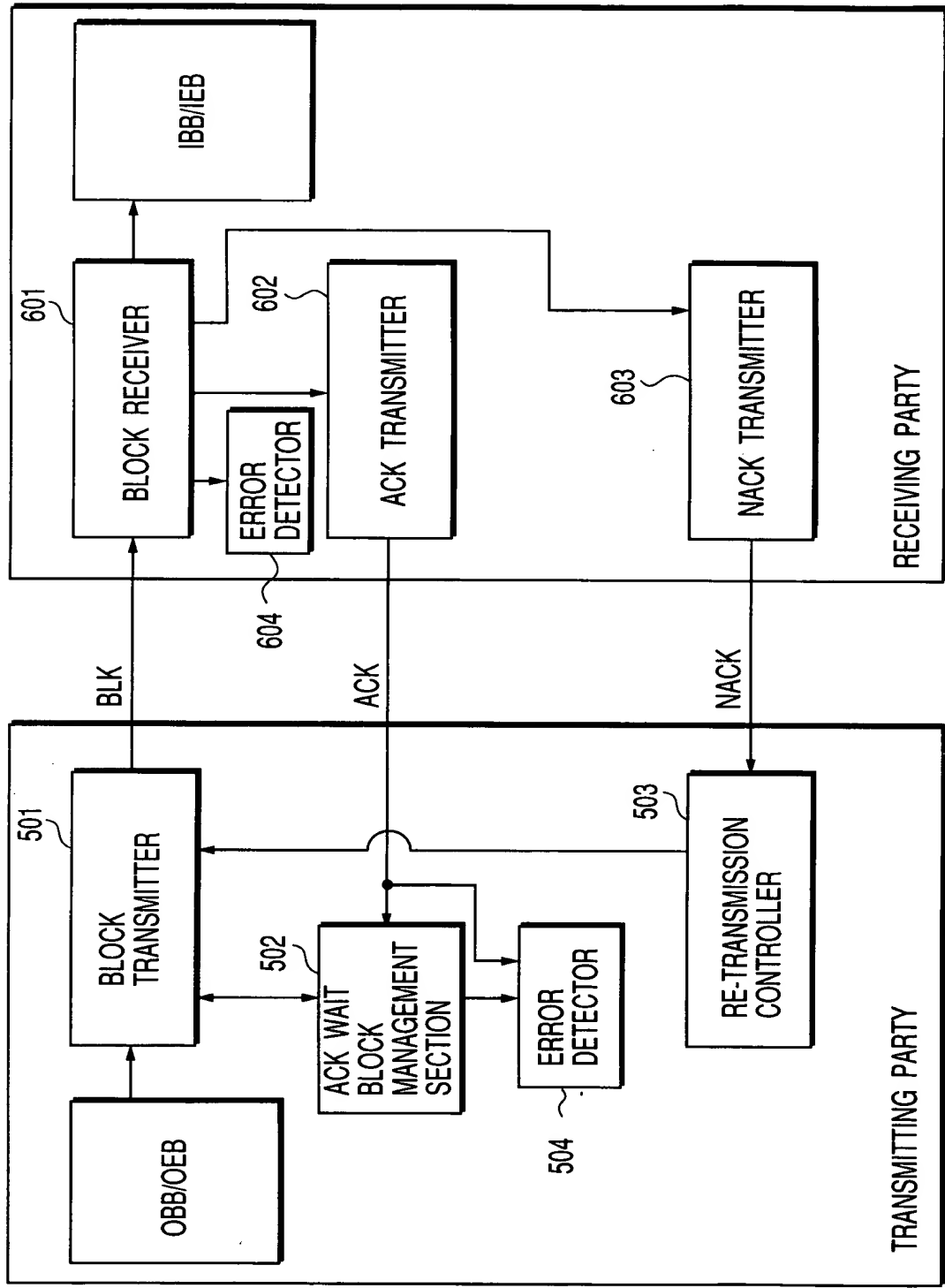
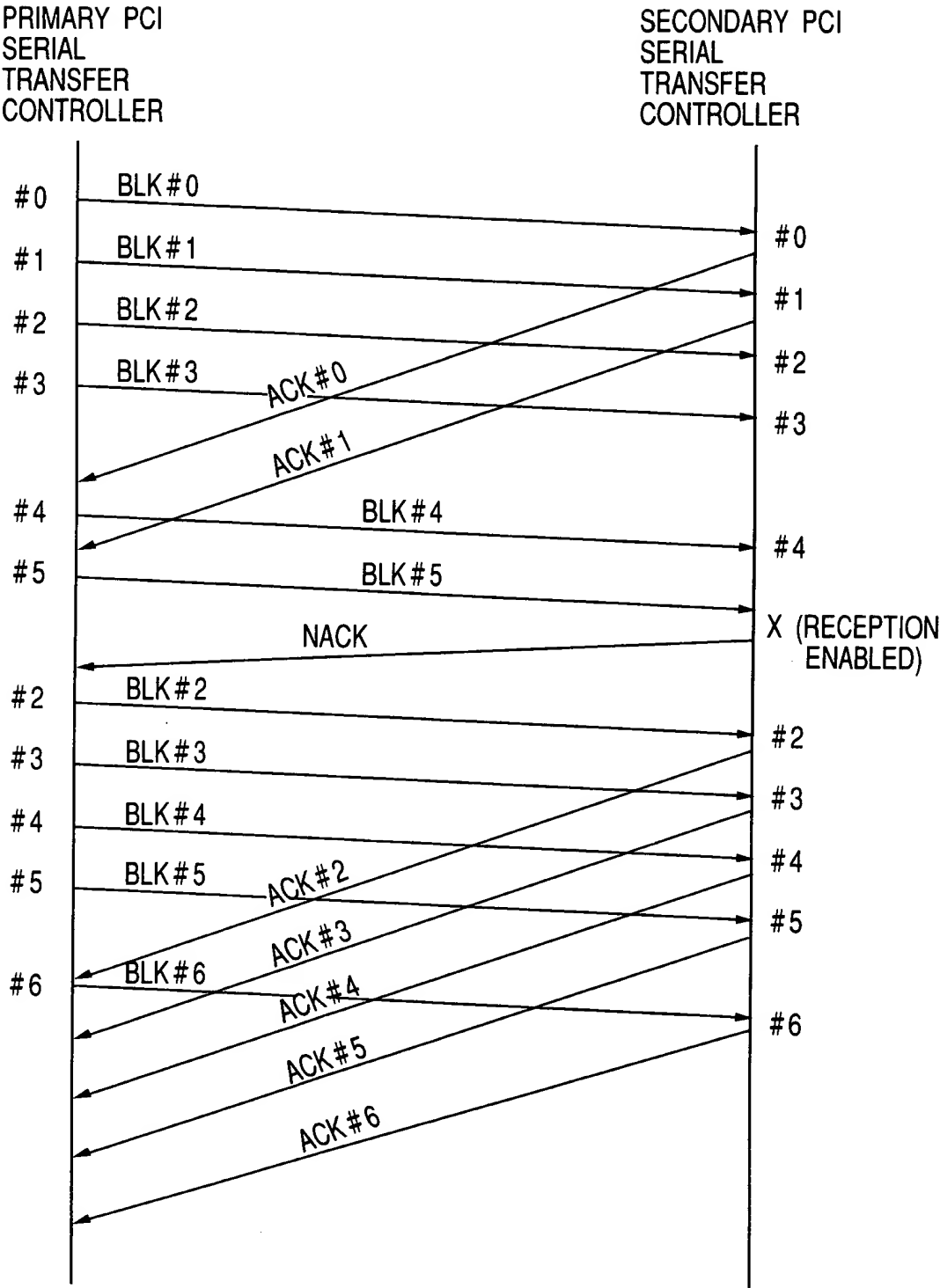


FIG. 8

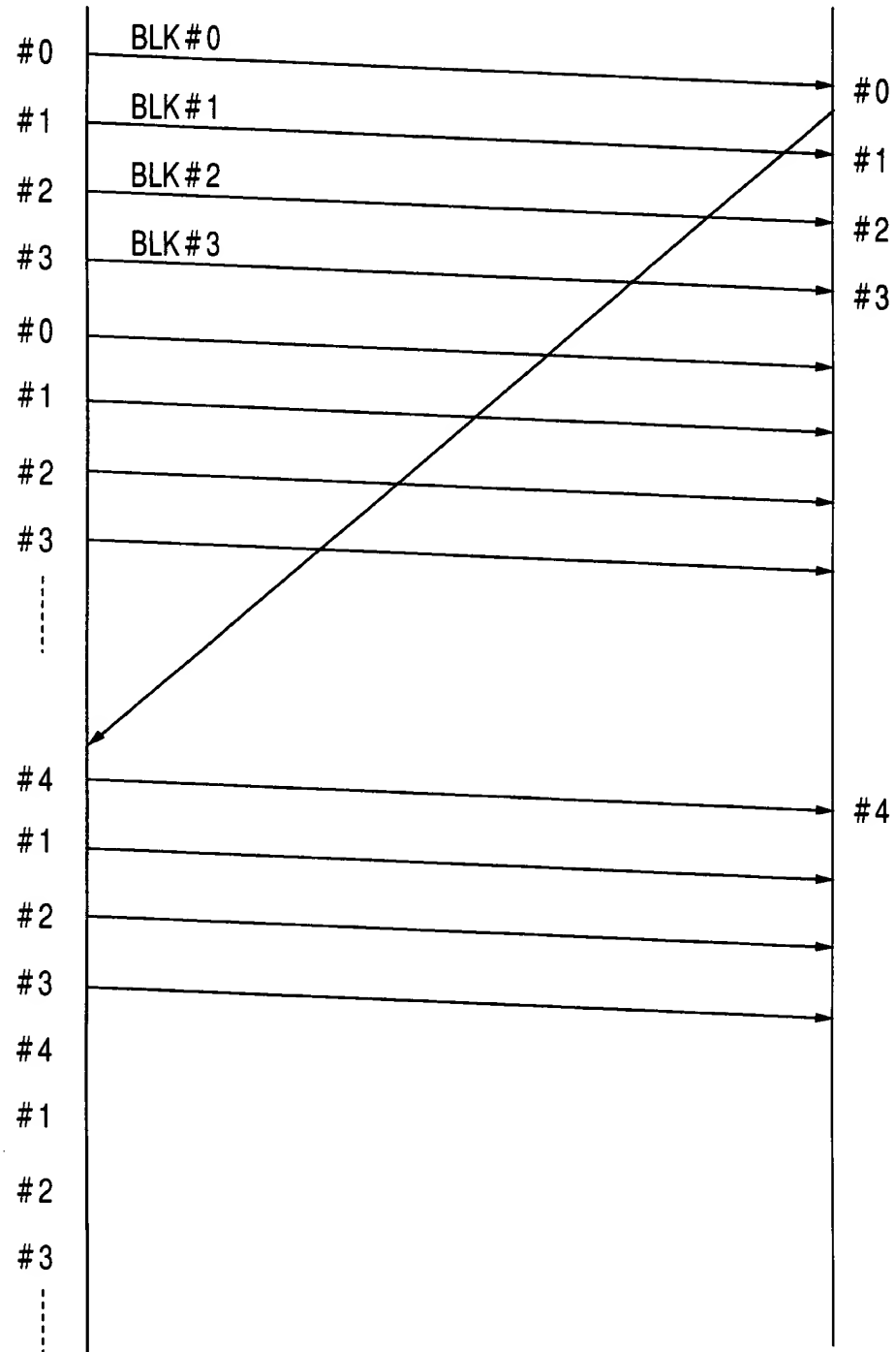


BLOCK TRANSFER & RE-TRANSMISSION CONTROL (EXAMPLE 1)

FIG. 9



## SECONDARY PCI SERIAL TRANSFER CONTROLLER



## BLOCK TRANSFER & RE-TRANSMISSION CONTROL (EXAMPLE 2)

FIG. 10

[illegible]

PRIMARY PCI  
SERIAL  
TRANSFER  
CONTROLLER